UCSB Commissioning Activities, Time Budget

1. Prepare 2 DFAs (pluck faults, retest)
2. Receive HDIs from Milan (1 day)
    - need results from Milan tests
    - need checklist for visual inspection
    - parts should be sexed, marked
3. Attach buttons, mount HDIs in ringframes (1 day)
4. Test, get up to speed with new chip (5 days)
5. Cut upilex, glue (3 days)
6. Bond (quickly, but we'll want some practice)
7. Test for shorts
8. Rework (steps 6-8: about 5 days)
9. Test in ringframe (2 days)

10. Encapsulate (1 day)

11. Assemble as module in its box (2 days)

    - What is the status of the tails?

12. Final test (2 days)

- Time estimate is intended to be realistic, given learning curve and doing what we can in parallel
- Should take about 22 working days, or about a month
- Will absorb UCSB human resources for that month